



501.40724X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: M. Sahara, et al.
Application No.: 09/974,814
Date: October 12, 2001
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
THE PROCESS OF THE SAME
Art Unit: 2814
Examiner: T. Quach

**SUBMISSION UNDER 37 CFR § 1.114
(AMENDMENT)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

September 7, 2004

Sir:

In response to the Office Action mailed May 5, 2004, please amend the above-identified application as listed in the following, and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.